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AMENDMENT

Amendments to the claims are reflected in the following listing, which replaces any and all prior versions and listings of claims in the present application:

1.-7. (Previously Canceled)

8. (Currently Amended) A stacked die package comprising:

a package substrate having a top side and a bottom side, the top side having a plurality of bond pads provided thereon, and the bottom side having a ball-grid array pattern provided thereon;

a first semiconductor device mounted on the top side of the package substrate, the first semiconductor device having a plurality of bond pads provided thereon;

a silicon interposer mounted on the first semiconductor device, the interposer having a first interposer bond pad and a second interposer bond pad, both lying outside a perimeter of the first semiconductor device, wherein the first and second interposer bond pads are electrically coupled via a conductive trace, and wherein the interposer includes: an interposer substrate; a dielectric layer formed on the interposer substrate; a conductive trace formed on the dielectric layer; and a passivation layer formed on the conductive trace, said passivation layer having a plurality of windows formed therein to expose the conductive trace in areas defining the interposer bond pads; and

a second semiconductor device mounted on the interposer, the second semiconductor device having a plurality of bond pads provided thereon, wherein at least a portion of said conductive trace lies at least partially directly underneath and insulated from said second semiconductor device;

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a first bond wire connected to one of the plurality of bond pads on said first semiconductor

and to the first interposer bond pad;

a second bond wire connected to the second interposer bond pad and to one of the plurality of

bond pads on the semiconductor device; and

a third bond wire connected to one of the plurality of bond pads on the top side of the

package substrate and to a bond pad on the first semiconductor device.

9. (Original) The stacked die package of claim 8, wherein said interposer substrate comprises silicon.
10. (Original) The stacked die package of claim 8, wherein said package substrate comprises a material selected from the group consisting of ceramic, polyimide, metal, glass-filled resin, cyanate-glass.
11. (Original) The stacked die package of claim 8, wherein said package substrate includes multilevel metallization.
12. (Original) The stacked die package of claim 8, wherein said interposer passivation layer comprises silicon nitride.
13. (Previously Canceled)